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FIR Filter. Simulate, synthesize & Place & Route. area/**Speed** Tradeoff. **FIR Filter** ... devices—embedded System Blocks (ESBs) or **embedded array Blocks** ...

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speed of pipelined operation). All the functions need to ... the approach for a general “parallel” **FIR filter** [2]. For reference, ...

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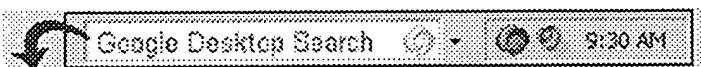
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These **factors**, combined with the time-to-market and flexibility of A ... **high-speed** finite impulse response (**FIR**) filters, and are targeted to A ...

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... with a high processing **speed** and flexibility is therefore a crucial **factor**.

... Once the basic concepts of **FIR filter** design are studied and understood ...

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... the received signal varies with the mobile **speed** due to ... On the stability boundary, the **gain factor** is given by ... in the implementation of the basis **FIR filter** $H(z)$...

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multiplication **factor**. Designers can implement the ... functions and high-speed adaptive finite impulse. response (**FIR**) filters, and the complete FLEX DSP ...

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Customer Application: Altera Delivers **Speed** for GigaNet. ATM Protocol Engine .

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Table 1 provides an overview of gate count, memory capacity, **speed**, ... 9-bit

parity checker or 400 to implement a 55-MHz, 8-bit, 16-tap parallel **FIR filter** ...

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... These **factors**, combined with the time-to-market ... features as well as distinct **speed** and utilization ... composed of a series of **embedded array blocks** (EABs), which ...

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